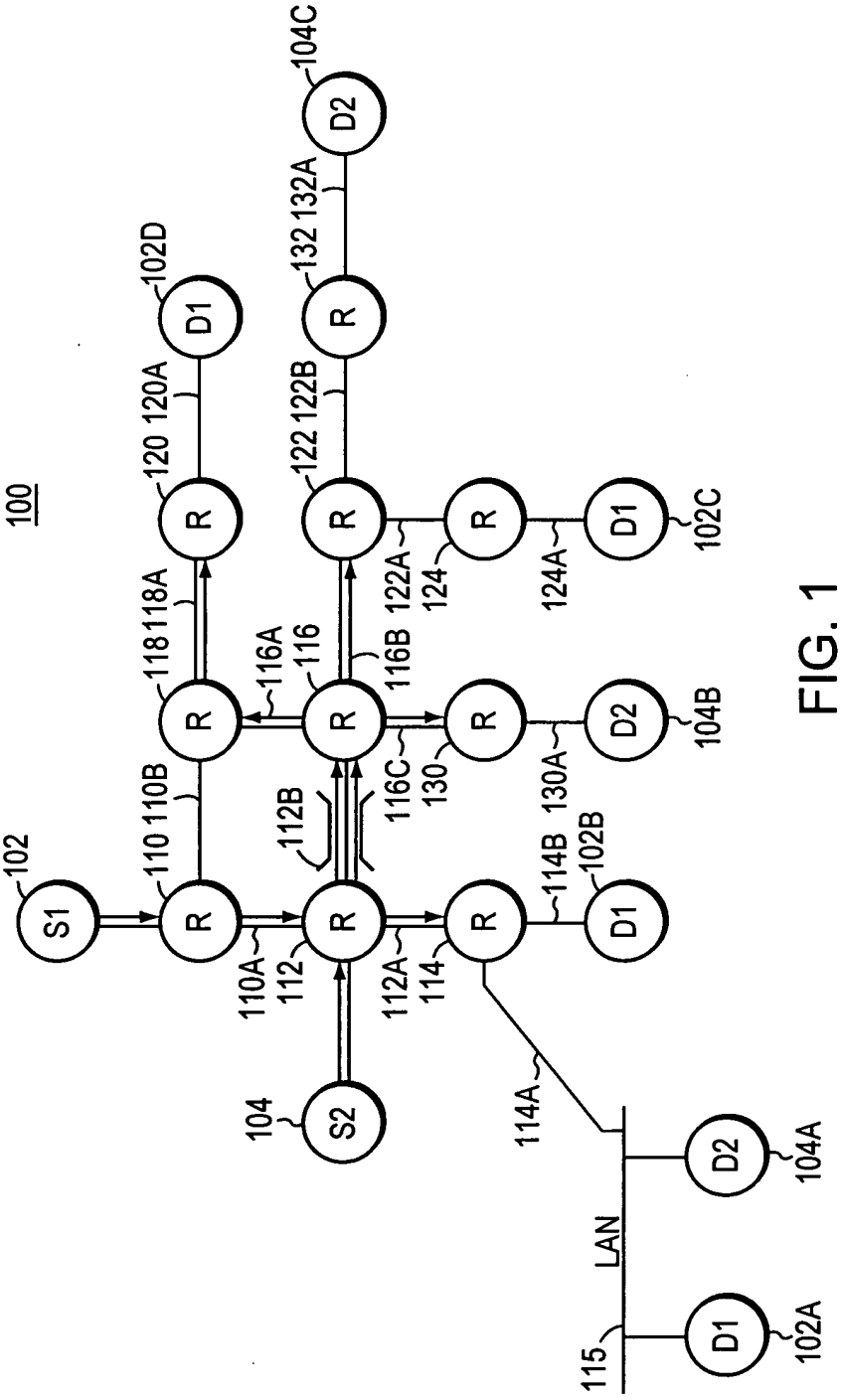

100
102
S1
110
110B
R
118
118A
120
120A
D1
102D
132
132A
D2
104C
122
122B
116
116A
116B
112B
116C
130
130A
D2
104B
122A
124
124A
D1
102C
110A
112
112A
114
114B
102B
D1
104
S2
114A
LAN
115
D2
104A
D1
102A

FIG. 1

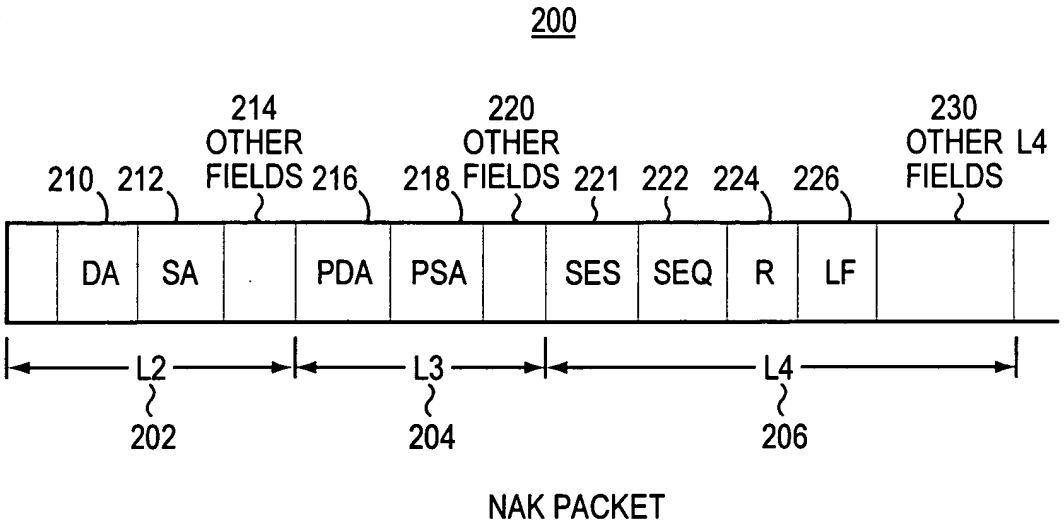

200
214
OTHER
FIELDS
220
OTHER
FIELDS
230
OTHER L4
FIELDS
210 212 216 218 221 222 224 226
DA
SA
PDA
PSA
SES
SEQ
R
LF
L2
L3
L4
202
204
206
NAK PACKET

FIG. 2

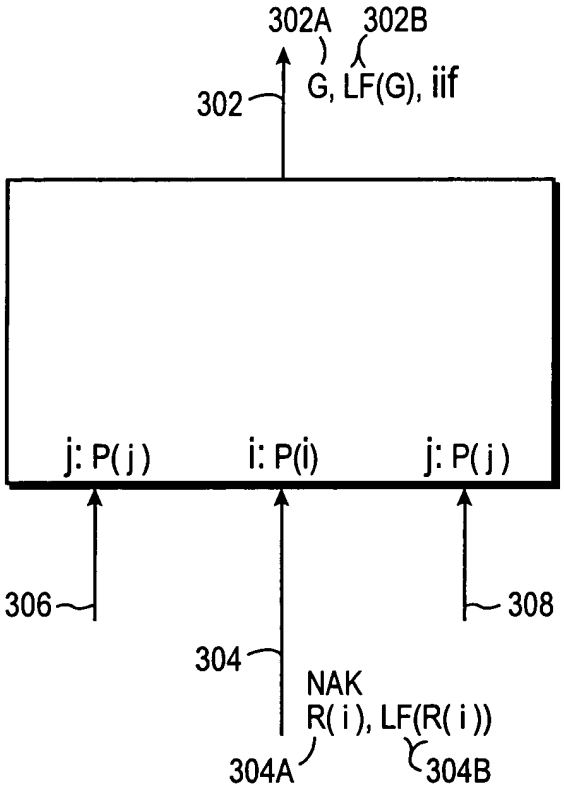
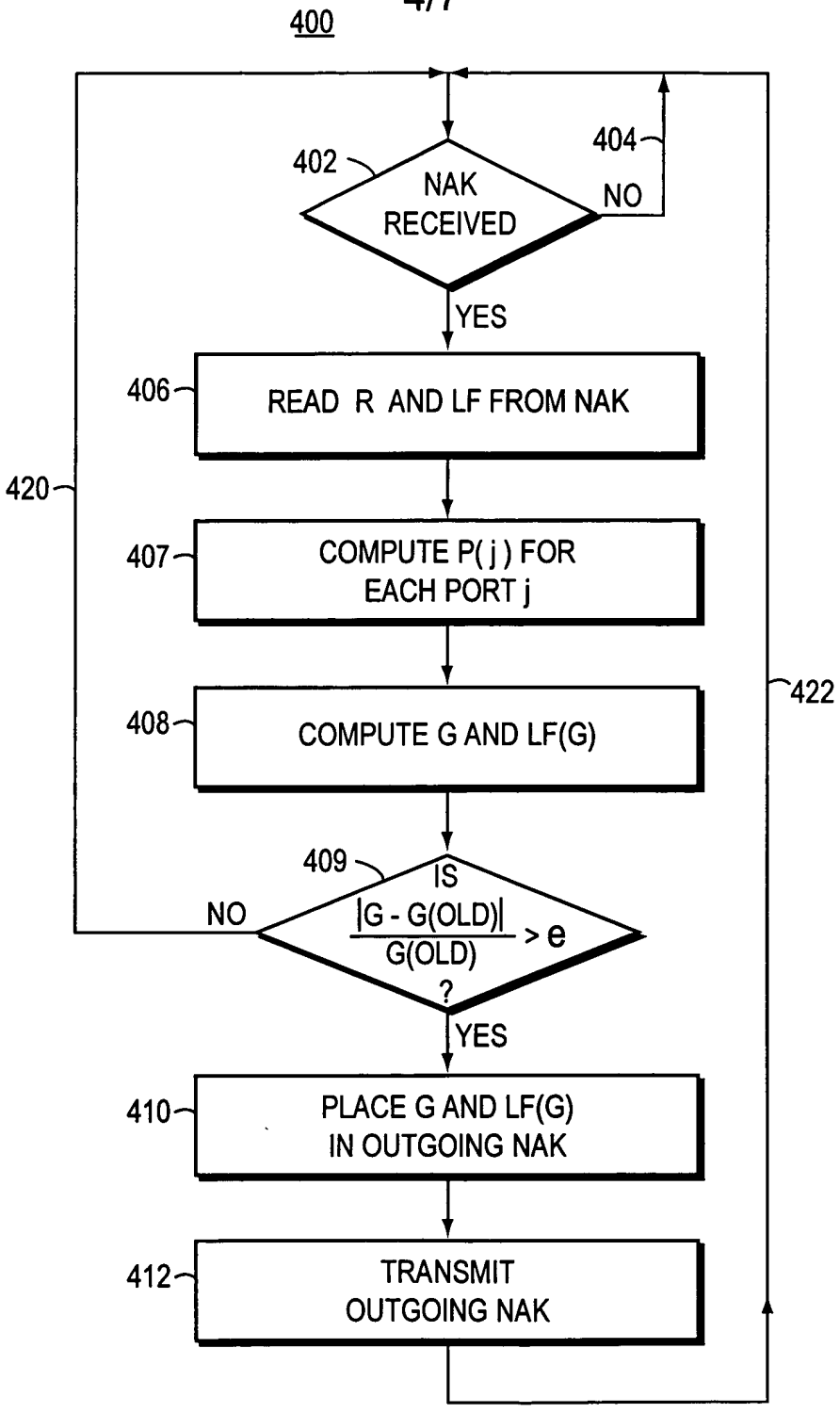

300
302A
302B
302
G, LF(G), iif
j: P(j)
i: P(i)
j: P(j)
306
308
304
NAK
R(i), LF(R(i))
304A
304B

FIG. 3



EVENT DRIVEN NAK TRANSMISSION

FIG. 4A

ALTERNATIVE BLOCK 407 CALCULATION

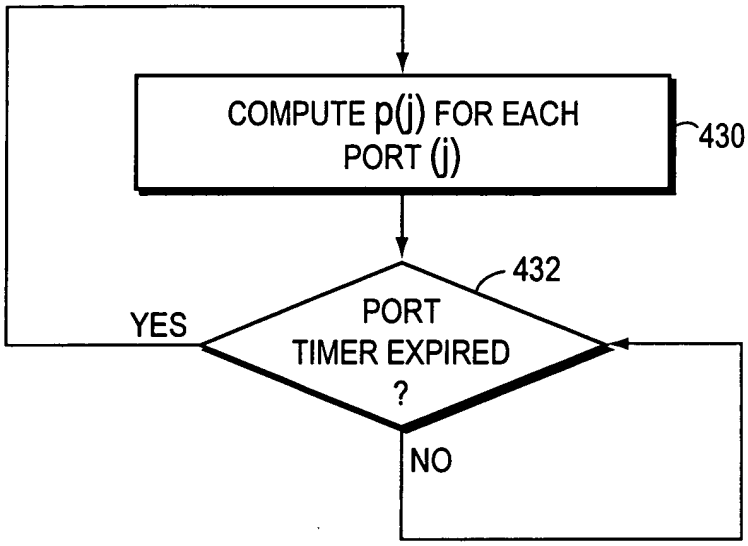
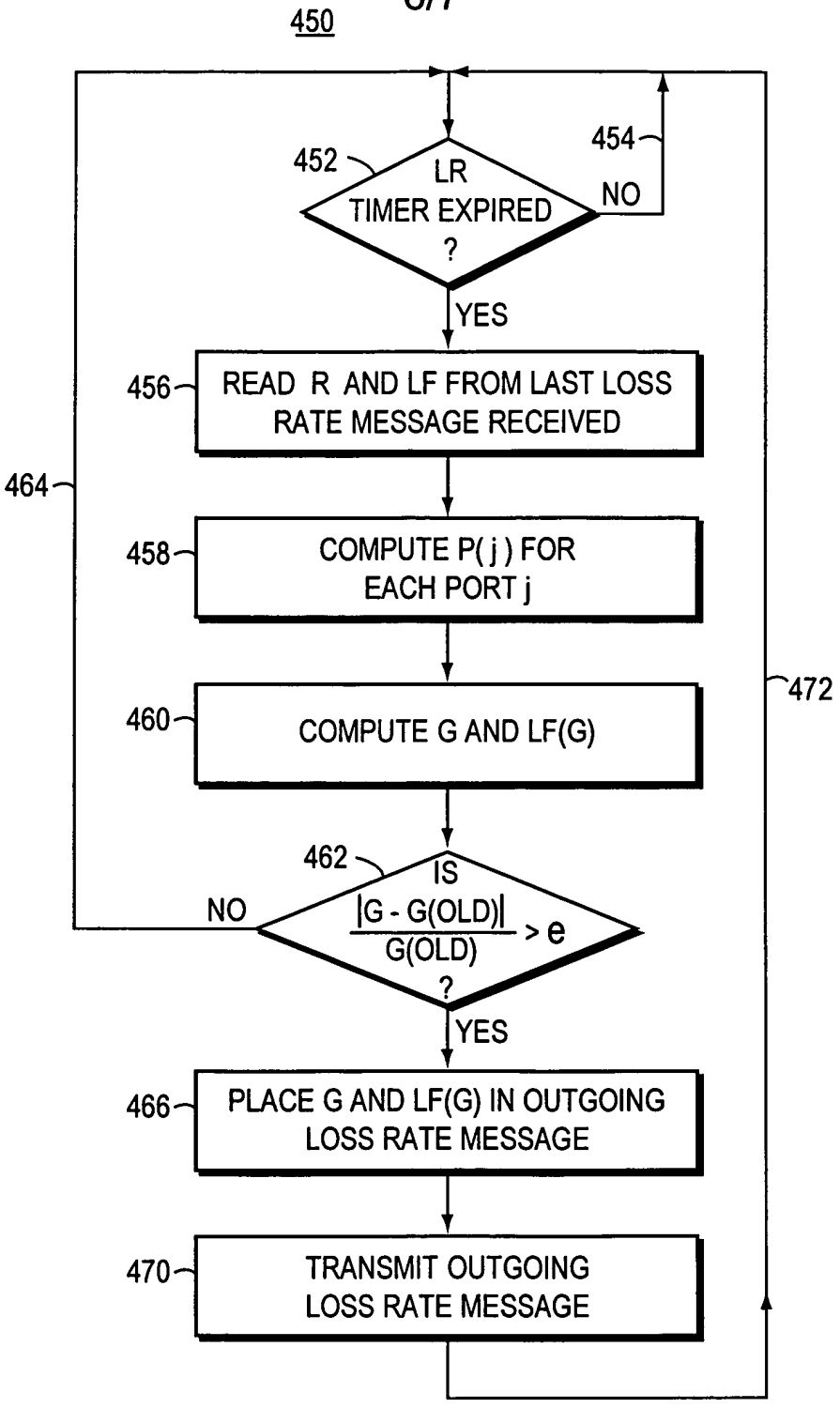


FIG. 4B



TIMER DRIVEN TRANSMISSION OF LOSS RATE MESSAGE

FIG. 4C

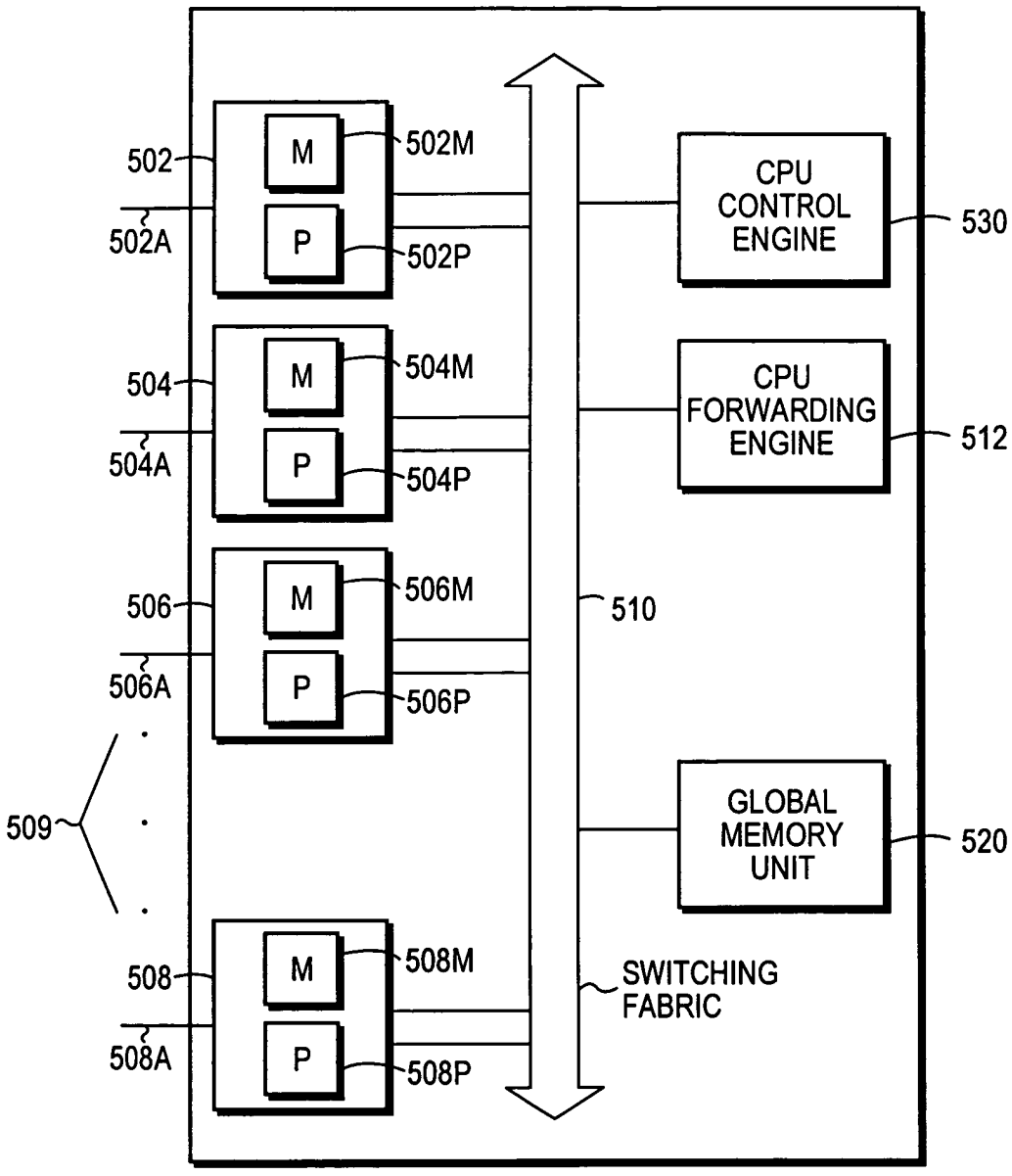

500
502
502M
M
P
502P
502A
504
504M
504P
504A
506
506M
506P
506A
509
508
508M
508P
508A
510
SWITCHING FABRIC
CPU CONTROL ENGINE
530
CPU FORWARDING ENGINE
512
GLOBAL MEMORY UNIT
520

FIG. 5